CLAIMS

Having thus described our invention, what we claim as new and desire to secure by Letters Patent is as follows:

1. A system for displaying information, comprising:

an extended bus bridge;

a graphics adaptor coupled to said extended bus bridge; and

a monitor coupled to said graphics adaptor to display the information, such that said graphics adaptor is localized to said monitor.

2. ~~The system according to claim 1, wherein said extended bus bridge includes first and second portions, a first portion being coupled to said graphics adaptor.~~

3. The system according to claim 2, further comprising a central processing unit (CPU) coupled to said second portion of said extended bus bridge.

4. ~~The system according to claim 3, further comprising a link~~ for coupling together said first and ~~second~~ portions of said extended ~~bus bridge.~~

5. The system according to claim 4, wherein said link comprises a serial link.

6. The system according to claim 4, wherein said link comprises at least one of a cable, a radio frequency (RF) link, and an infrared (IR) link.

7. The system according to claim 1, wherein said extended bus bridge comprises a peripheral component interconnect (PCI) bus bridge.

8. The system according to claim 1, wherein said extended bus bridge comprises an Accelerated Graphics Port (AGP) bus bridge.

9. A display unit, comprising:

at least a portion of an extended bus bridge;

a graphics adaptor coupled to said at least ~~portion of the~~ extended bus bridge; and

a monitor coupled to said graphics adaptor to display ~~the~~ information, such that said graphics adaptor is localized to said monitor.

10. The display unit according to claim 9, wherein said at least a portion of said extended bus bridge comprises one side of said extended bus bridge embedded in said adaptor.

11. ~~The display unit according to claim 9, wherein said extended bus bridge~~ includes first and ~~second portions~~, a first portion being coupled to said graphics adaptor.

12. The display unit according to claim 9, wherein said extended bus bridge comprises a ~~peripheral component~~ interconnect (PCI) bus bridge.

13. The system according to claim 9, wherein said extended bus bridge comprises an Accelerated Graphics Port (AGP) bus bridge.

14. A method of decreasing a bottleneck in a communications bus, comprising:

coupling a graphics adaptor, a central processing unit (CPU) and a display monitor over said communications bus;

providing an extended bus bridge between said graphics adaptor and said central processing unit (CPU); and

localizing said graphics adaptor to said display monitor.

15. ~~The method according to claim 14, wherein said extended bus bridge includes first and second portions, a first portion being coupled to said graphics adaptor.~~

16. The method according to claim 15, wherein said central processing unit (CPU) coupled to said second portion of said extended bus bridge.

17. ~~The method according to claim 16,~~ further comprising coupling together said first and second ~~portions of said extended bus bridge via a link.~~

18. The method according to claim 17, wherein said link comprises a serial link.

19. The method according to claim 17, wherein said link comprises at least one of a cable, a radio frequency (RF) link, and an infrared (IR) link.

20. The method according to claim 14, wherein said extended bus bridge comprises a peripheral component interconnect (PCI) bus bridge.

21. The method according to claim 14, wherein said extended bus bridge comprises an Accelerated Graphics Port (AGP) bus bridge.

Add B4

Add D1